

XT1S104 Datasheet

Ver-1.1

General Description

The XT1S104 is a modular, high-performance, low additive jitter, low-skew, general purpose clock buffer.

The entire family is designed with a modular approach in mind. All of the devices are pin-compatible to each other for easy handling.

The XT1S104 family operates in a 1.8-V, 2.5 V and 3.3V environment and are characterized for operation from -40°C to 85°C .

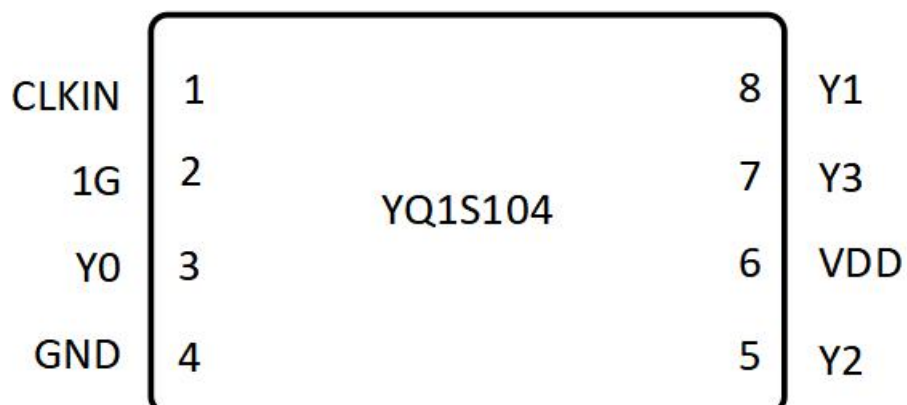
Features

- High-Performance 1:4 LVCMOS Clock Buffer
- Very Low Pin-to-Pin Skew < 50 ps
- Very Low Additive Jitter < 50 fs
- Supply Voltage: 1.8-V, 2.5-V or 3.3-V
- $F_{\text{max}} = 250\text{ MHz}$ for 3.3-V
 $F_{\text{max}} = 200\text{ MHz}$ for 2.5-V/1.8-V
- Operating Temperature Range: -40°C to 85°C
Available TSSOP8 Package (All Pin-Compatible)

Applications

- General purpose communication,
- Industrial
- Consumer applications

Pin Configuration:



Pin Descriptions:

| Number | Name | Type | Description |
|--------|-------|--------|---------------------------------------|
| 1 | CLKIN | Input | Input Clock Pin |
| 2 | 1G | Input | Clock Output Enable |
| 3 | Y0 | Output | LVC MOS output |
| 4 | GND | Ground | Ground |
| 5 | Y2 | Output | LVC MOS output |
| 6 | VDD | Power | Supply Voltage: 1.8-V, 2.5-V or 3.3-V |
| 7 | Y3 | Output | LVC MOS output |
| 8 | Y1 | Output | LVC MOS output |